<table>
<tr><td>Name</td><td colspan="3">Dr. MAHESH P K</td><td rowspan="3"></td></tr>
<tr><td>Date of Birth</td><td colspan="3">24.02.1979</td></tr>
<tr><td>Unique ID (AICTE ID)</td><td colspan="3">1-43386614911</td></tr>
<tr><td rowspan="2">Education Qualifications</td><td>Ph.D.</td><td>PG</td><td colspan="2">UG</td></tr>
<tr><td>Yes</td><td>Yes</td><td colspan="2">Yes</td></tr>
<tr><td rowspan="2">Work Experience</td><td>Teaching</td><td colspan="2">Research</td><td>Industry</td><td>Others</td></tr>
<tr><td>22</td><td colspan="2">12</td><td>--</td><td>--</td></tr>
<tr><td>Area of Specialization</td><td colspan="4">Pattern Recognition, Image Processing, Signal Processing and Cryptography</td></tr>
<tr><td>Courses taught</td><td colspan="4">VLSI, Embedded System, Verilog HDL, Digital Signal Processing.</td></tr>
<tr><td>Research guidance
(Number of Students)</td><td colspan="4">07</td></tr>
<tr><td>No. of papers published</td><td>National</td><td>International</td><td colspan="2">Conferences</td></tr>
<tr><td></td><td>10</td><td>32</td><td colspan="2">25</td></tr>
<tr><td>Projects Carried out</td><td colspan="4">02</td></tr>
<tr><td>Patents (Filed & Granted)</td><td colspan="4">01</td></tr>
<tr><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="4">10</td></tr>
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